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MICROELECTRONIC CIRCUIT DESIGN. FIFTH EDITION Richard C. Jaeger Distinguished University Professor Emeritus ECE Department Auburn University jaegerc@auburn.edu and Travis N. Blalock Visiting Associate Professor ECE Department University of Virginia blalock@virginia.edu

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Microelectronic Circuit Design is known for being a technically excellent text. The new edition has been revised to make the material more motivating and accessible to students while retaining a student-friendly approach. Jaeger has added more pedagogy and an emphasis on design through the use of design examples and design notes. Some pedagogical elements include chapter opening vignettes, chapter objectives, "Electronics in Action" boxes, a problem solving methodology, and "design note" boxes. The number of examples, including new design examples, has been increased, giving students more opportunity to see problems worked out. Additionally, some of the less fundamental mathematical material has been moved to the ARIS website. In addition this edition comes with a Homework Management System called ARIS, which includes 450 static problems.

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Richard Jaeger and Travis Blalock present a balanced coverage of analog and digital circuits; students will develop a comprehensive understanding of the basic techniques of modern electronic circuit design, analog and digital, discrete and integrated. A broad spectrum of topics are included in Microelectronic Circuit Design which gives the professor the option to easily select and customize the material to satisfy a two-semester or three-quarter sequence in electronics. Jaeger/Blalock emphasizes design through the use of design examples and design notes. Excellent pedagogical elements include chapter opening vignettes, chapter objectives, "Electronics in Action" boxes, a problem-solving methodology, and "Design Note" boxes. The use of the well-defined problem-solving methodology presented in this text can significantly enhance an engineer's ability to understand the issues related to design. The design examples assist in building and understanding the design process.

This introductory book assumes minimal knowledge of the existence of integrated circuits and of the terminal behavior of electronic components such as resistors, diodes, and MOS and bipolar transistors. It presents to readers the basic information necessary for more advanced processing and design books. Focuses mainly on the basic processes used in fabrication, including lithography, oxidation, diffusion, ion implementation, and thin film deposition. Covers interconnection technology, packaging, and yield. Appropriate for readers interested in the area of fabrication of solid state devices and integrated circuits.

This junior level electronics text provides a foundation for analyzing and designing analog and digital electronics throughout the book. Extensive pedagogical features including numerous design examples, problem solving technique sections, Test Your Understanding questions, and chapter checkpoints lend to this classic text. The author, Don Neamen, has many years experience as an Engineering Educator. His

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experience shines through each chapter of the book, rich with realistic examples and practical rules of thumb. The Third Edition continues to offer the same hallmark features that made the previous editions such a success. Extensive Pedagogy: A short introduction at the beginning of each chapter links the new chapter to the material presented in previous chapters. The objectives of the chapter are then presented in the Preview section and then are listed in bullet form for easy reference. Test Your Understanding Exercise Problems with provided answers have all been updated. Design Applications are included at the end of chapters. A specific electronic design related to that chapter is presented. The various stages in the design of an electronic thermometer are explained throughout the text. Specific Design Problems and Examples are highlighted throughout as well.

Praise for CMOS: Circuit Design, Layout, and Simulation Revised Second Edition from the Technical Reviewers "A refreshing industrial flavor. Design concepts are presented as they are needed for 'just-in-time' learning. Simulating and designing circuits using SPICE is emphasized with literally hundreds of examples. Very few textbooks contain as much detail as this one. Highly recommended!" --Paul M. Furth, New Mexico State University "This book builds a solid knowledge of CMOS circuit design from the ground up. With coverage of process integration, layout, analog and digital models, noise mechanisms, memory circuits, references, amplifiers, PLLs/DLLs, dynamic circuits, and data converters, the text is an excellent reference for both experienced and novice designers alike." --Tyler J. Gomm, Design Engineer, Micron Technology, Inc. "The Second Edition builds upon the success of the first with new chapters that cover additional material such as oversampled converters and non-volatile memories. This is becoming the de facto standard textbook to have on every analog and mixed-signal designer's bookshelf." --Joe Walsh, Design Engineer, AMI Semiconductor CMOS circuits from design to implementation CMOS: Circuit Design, Layout, and Simulation, Revised Second Edition covers the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks, the BSIM model, data converter architectures, and much more. This edition takes a two-path approach to the topics: design techniques are developed for both long- and short-channel CMOS technologies and then compared. The results are multidimensional explanations that allow readers to gain deep insight into the design process. Features include: Updated materials to reflect CMOS technology's movement into nanometer sizes Discussions on phase- and delay-locked loops, mixed-signal circuits, data converters, and circuit noise More than 1,000 figures, 200 examples, and over 500 end-of-chapter problems In-depth coverage of both analog and digital circuit-level design techniques Real-world process parameters and design rules The book's Web site, CMOSedu.com, provides: solutions to the book's problems; additional homework problems without solutions; SPICE simulation examples using HSPICE, LTspice, and WinSpice; layout tools and examples for actually fabricating a chip; and videos to aid

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The Third Edition of CMOS Circuit Design, Layout, and Simulation continues to cover the practical design of both analog and digital integrated circuits, offering a vital, contemporary view of a wide range of analog/digital circuit blocks including: phase-locked-loops, delta-sigma sensing circuits, voltage/current references, op-amps, the design of data converters, and much more. Regardless of one's integrated circuit (IC) design skill level, this book allows readers to experience both the theory behind, and the hands-on implementation of, complementary metal oxide semiconductor (CMOS) IC design via detailed derivations, discussions, and hundreds of design, layout, and simulation examples.

STUDENT COMPANION SITE Every new copy of Stuart Wentworth's Applied Electromagnetics comes with a registration code which allows access to the Student's Book Companion Site. On the BCS the student will find: * Detailed Solutions to Odd-Numbered Problems in the text * Detailed Solutions to all Drill Problems from the text * MATLAB code for all the MATLAB examples in the text * Additional MATLAB demonstrations with code. This includes a Transmission Lines simulator created by the author. * Weblinks to a vast array of resources for the engineering student. Go to www.wiley.com/college/wentworth to link to Applied Electromagnetics and the Student Companion Site. ABOUT THE PHOTO Passive RFID systems, consisting of readers and tags, are expected to replace bar codes as the primary means of identification, inventory and billing of everyday items. The tags typically consist of an RFID chip placed on a flexible film containing a planar antenna. The antenna captures radiation from the reader's signal to power the tag electronics, which then responds to the reader's query. The PENI Tag (Product Emitting Numbering Identification Tag) shown, developed by the University of Pittsburgh in a team led by Professor Marlin H. Mickle, integrates the antenna with the rest of the tag electronics. RFID systems involve many electromagnetics concepts, including antennas, radiation, transmission lines, and microwave circuit components. (Photo courtesy of Marlin H. Mickle.)

Ideal for a one-semester course, this concise textbook covers basic electronics for undergraduate students in science and engineering. Beginning with the basics of general circuit laws and resistor circuits to ease students into the subject, the textbook then covers a wide range of topics, from passive circuits through to semiconductor-based analog circuits and basic digital circuits. Using a balance of thorough analysis and insight, readers are shown how to work with electronic circuits and apply the techniques they have learnt. The textbook's structure makes it useful as a self-study introduction to the subject. All mathematics is kept to a suitable level, and there are several exercises throughout the book. Password-protected solutions for instructors, together with eight

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